

(19)



Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 1 043 618 A1

(12)

EUROPEAN PATENT APPLICATION

published in accordance with Art. 158(3) EPC

(43) Date of publication:

11.10.2000 Bulletin 2000/41

(21) Application number: 99949373.7

(22) Date of filing: 22.10.1999

(51) Int. Cl.⁷: G02F 1/133, G09G 3/36

(86) International application number:

PCT/JP99/05847

(87) International publication number:

WO 00/23848 (27.04.2000 Gazette 2000/17)

(84) Designated Contracting States:

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE

(30) Priority: 22.10.1998 JP 30060698

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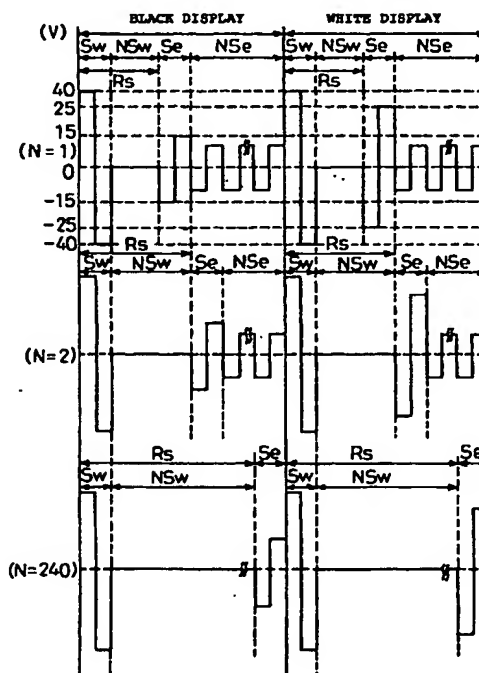
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Wilhelm & Dauster
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70174 Stuttgart (DE)**(54) FERROELECTRIC LIQUID CRYSTAL DISPLAY, AND ITS DRIVING METHOD**

(57) A ferroelectric liquid crystal display element holds a ferroelectric liquid crystal between a pair of substrates. A driving scheme includes a reset period having a switching period and a non-switching period which precede application of a selection pulse. During the switching period, a voltage exceeding a threshold voltage required to switch the ferroelectric liquid crystal is applied irrespective of the display data to be written to a pixel, and during the non-switching period, an ion electric field in the ferroelectric liquid crystal is aligned in such a direction that cancels the direction of an electric field produced by spontaneous polarization of the liquid crystal molecules. A voltage not exceeding the threshold voltage of the ferroelectric liquid crystal, preferably a voltage of 0 V, that is, no voltage, is applied during the non-switching period.

Fig.11



EP 1 043 618 A1

Description

TECHNICAL FIELD

[0001] The present invention relates to a ferroelectric liquid crystal display device; more particularly, the invention relates to a liquid crystal display device wherein an image sticking phenomenon of a liquid crystal display screen is eliminated by preventing the threshold voltage of the liquid crystal from changing, and also relates to a method for driving the same.

BACKGROUND ART

[0002] In recent years, research and development has been carried out vigorously on liquid crystal display devices that utilize ferroelectric liquid crystals having properties, such as fast response speed and memory effect, that distinguish them from conventional liquid crystals. Liquid crystal display elements utilizing ferroelectric liquid crystals are finding widespread use in such applications as display apparatuses and liquid crystal shutters.

[0003] Figure 1 is a diagram showing the arrangement of polarizers when the ferroelectric liquid crystal is used as a liquid crystal display element. A liquid crystal cell 2 is placed between the polarizers 1a and 1b arranged in a crossed nicols configuration in such a manner that the long axis direction of liquid crystal molecules when the molecules are in a first stable state or in a second stable state is substantially parallel to either the polarization axis, a, of the polarizer 1a or the polarization axis, b, of the polarizer 1b.

[0004] When voltage is applied across the thus arranged liquid crystal cell, its light transmittance varies with the applied voltage, describing a loop as plotted in the graph of Figure 2.

[0005] The switching of the ferroelectric liquid crystal, that is, a transition from one stable state to the other stable state, occurs only when a voltage of such a value that the product of the width value and crest value of the applied voltage waveform is larger than the threshold value is applied to the ferroelectric liquid crystal molecules. As shown in Figure 2, either the first stable state (non-transmission (black) display state) or the second stable state (transmission (white) display state) is selected depending on the polarity of the applied voltage.

[0006] Here, the voltage value at which the light transmittance begins to change when the applied voltage is increased is denoted by V_1 , and the voltage value at which the light transmittance reaches saturation is denoted by V_2 ; on the other hand, when the applied voltage is decreased and a voltage of opposite polarity is applied, the voltage value at which the light transmittance begins to drop is denoted by V_3 , and the voltage value at and beyond which the light transmittance does not drop further is denoted by V_4 . As shown

in Figure 2, the second stable state is selected when the value of the applied voltage is greater than the threshold value of the ferroelectric liquid crystal molecules. When a voltage of opposite polarity greater in magnitude than the threshold value of the ferroelectric liquid crystal molecules is applied, the first stable state is selected.

[0007] When the polarizers are arranged as shown in Figure 1, a white display (transmission state) can be produced in the second stable state and a black display (non-transmission state) in the first stable state. The arrangement of the polarizers can be changed so that the black display (non-transmission state) is produced in the second stable state and the white display state (transmission state) in the first stable state. The description hereinafter given, however, assumes that the white display (transmission state) is produced in the second stable state and the black display (non-transmission state) in the first stable state.

[0008] As one method for driving a ferroelectric liquid crystal display device, a time division driving method has been known in the art. In the time division driving method, a plurality of scanning electrodes and signal electrodes are formed on the substrates, and the liquid crystal display device is driven by applying voltages to the respective electrodes. Figure 3 shows a driving scheme used in the time division driving method for driving the ferroelectric liquid crystal. As shown in Figure 3, writing to a pixel is done by applying a scanning voltage (a) to its associated scanning electrode and a signal voltage (b) to its associated signal electrode, thereby applying their combined voltage (c) to the pixel. In the figure, (d) shows the light transmittance of the liquid crystal display device. Usually, each selection period (Se) is preceded by a reset period (Rs). NSe denotes a non-selection period. In the case of Figure 3, the pixel is forcibly reset to the black display state (the first stable state) in the reset period (Rs) irrespective of its display data.

[0009] As shown in the left-hand half of Figure 3, when producing a black display, a voltage that, as the combined voltage (c), does not exceed the threshold value is applied during the selection period (Se), and the state achieved in the reset period (Rs) is thus retained. On the other hand, as shown in the right-hand half of Figure 3, when producing a white display, a voltage that, as the combined voltage (c), exceeds the threshold value for the second stable state, is applied during the selection period (Se).

[0010] In the conventional art, it is known to apply during the reset period, a voltage scheme consisting of a single-polarity pulse or a bipolar pulse or a succession of bipolar pulses.

[0011] Ferroelectric liquid crystals exhibit spontaneous polarization and hence have the memory effect by which the written display state is maintained. However, if this memory effect is too strong, a phenomenon known as an image sticking phenomenon, in which the previously written display state persists and interferes

with the writing of a new display state, becomes evident.

[0012] To prevent such an image sticking phenomenon, it has traditionally been practiced to form an alignment film using a material that has the property of preventing ionic impurities from adhering in the vicinity of the alignment film. Furthermore, when not using the display for a long period of time, it has been practiced to put the entire display screen into a mixed state of microscopic white or black regions and hold it in this state, thereby canceling, in the liquid crystal display screen as a whole, the internal electric field produced by the spontaneous polarization within the cell.

[0013] It has generally been believed that such an image sticking phenomenon occurs after the screen has been held in the same display state for a long period of time. The inventor, however, has discovered that one of the causes of image sticking phenomenon is a change in the threshold voltage required to cause a transition from the first stable state to the second stable state, and that the image sticking phenomenon can occur in a short period of time. It has also been found that the reset period employed in the prior art is not sufficient to prevent the image sticking phenomenon.

[0014] In view of the above situation, it is an object of the present invention to resolve the problem of image sticking phenomenon by providing a driving method which prevents such a threshold voltage change occurring in a short period of time.

DISCLOSURE OF THE INVENTION

[0015] To achieve the above object, the ferroelectric liquid crystal display element of the present invention comprises a ferroelectric liquid crystal between a pair of substrates, and a drive waveform for driving the liquid crystal includes a reset period having a switching period and a non-switching period which precede a selection period wherein, during the switching period, a voltage exceeding a threshold voltage required to switch the ferroelectric liquid crystal is applied irrespective of the display data to be written to a pixel, and during the non-switching period, an ion electric field in the ferroelectric liquid crystal is aligned in such a direction that it cancels the direction of an electric field produced by spontaneous polarization of liquid crystal molecules.

[0016] For example, a voltage applied during the non-switching period does not exceed the threshold voltage of the ferroelectric liquid crystal. Preferably, a voltage of 0 V, that is, no voltage, is applied.

[0017] The voltage applied during the switching period is a set of bipolar pulses.

[0018] The length ΔT of the non-switching period is set so as to minimize the change in the threshold voltage.

[0019] The reset period is set with the same timing for all pixels.

[0020] Alternatively, the reset period is set each time display data is written to a pixel.

[0021] In that case, the length of the non-switching period is the same for all pixels.

[0022] At the end of a display operation of the ferroelectric liquid crystal display device, a switching pulse as applied during the reset period is applied to all the pixels to set them in the resulting state.

ADVANTAGEOUS EFFECT OF THE INVENTION

[0023] Using the ferroelectric liquid crystal display element of the invention or the driving method for the same, it becomes possible to suppress the change in the threshold voltage which occurs due to the display state of the liquid crystal panel. This serves to prevent the image sticking phenomenon caused by the change in the threshold voltage. This also serves to reduce the display flicker occurring during the non-selection period, allow a greater margin of driving with respect to temperature variations, and achieve a good display.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024]

Figure 1 is a diagram showing the structure of a ferroelectric liquid crystal display element used in the present invention.

Figure 2 is a diagram showing how the light transmittance of the ferroelectric liquid crystal display element used in the present invention varies with an applied voltage.

Figure 3 is a diagram showing a conventional art driving method for a ferroelectric liquid crystal display element.

Figure 4 is a diagram for explaining how an image sticking phenomenon occurs in the ferroelectric liquid crystal display element.

Figure 5 is a diagram showing the relationship between the light transmittance and the applied voltage when the ferroelectric liquid crystal display element is driven by the conventional art driving method.

Figure 6 is a diagram showing a driving scheme for the ferroelectric liquid crystal display element according to the present invention.

Figure 7 is a diagram showing the change in the threshold voltage of the ferroelectric liquid crystal as a function of the length ΔT of a non-switching period in a driving method for the ferroelectric liquid crystal display element according to the present invention.

Figure 8 is a diagram for explaining a mechanism that causes a change in the threshold voltage of the ferroelectric liquid crystal display element.

Figure 9 is a diagram for explaining a mechanism that causes a change in the threshold voltage of the ferroelectric liquid crystal display element.

Figure 10 is a diagram showing the structure of a

ferroelectric liquid crystal panel used in the present invention.

Figure 11 is a diagram showing a driving scheme for the ferroelectric liquid crystal display element according to an embodiment of the present invention.

Figure 12 is a diagram showing the relationship between the light transmittance and the applied voltage when the ferroelectric liquid crystal display element is driven according to the present invention.

Figure 13 is a diagram showing a driving scheme for the ferroelectric liquid crystal display element according to another embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0025] Figure 4 is a diagram for explaining how image sticking phenomenon occurs in a ferroelectric liquid crystal. Figure 4 shows a model illustrating the ion polarity and the direction of the spontaneous polarization that the liquid crystal molecules exhibit when the ferroelectric liquid crystal is sandwiched between a pair of glass substrates 11a and 11b.

[0026] Since ferroelectric liquid crystal molecules possess spontaneous polarization, ionic impurities remaining in the ferroelectric liquid crystal are attracted to the surfaces of the alignment films by the action of the internal electric field (EPs) produced within the cell by the spontaneous polarization, and an ion electric field (Eion) occurs in a direction that cancels the EPs. This phenomenon causes a change from the state of Figure 4(a) to the state of Figure 4(b). As shown in Figure 4(c), when an external voltage (Eout) is applied, switching occurs in the liquid crystal molecules. When the external voltage is removed, the liquid crystal molecules should retain the orientation shown in Figure 4(c). In reality, however, the orientation of the liquid crystal molecules is reversed by the action of the ion electric field (Eion), as shown in Figure 4(d). This is believed to cause the image sticking phenomenon.

[0027] As earlier described, it has generally been believed that image sticking phenomenon occurs because the same display state has been maintained for a long period of time. The inventor, however, has discovered that the image sticking phenomenon can occur in a short period of time for the reason described above.

[0028] The image sticking phenomenon that occurs in a short period of time will be explained with reference to Figure 5. The ferroelectric liquid crystal display device is driven using the conventional driving method illustrated, for example, in Figure 3. The voltage value of the selection pulse applied during the selection period is increased starting from 0 V. When the applied voltage exceeds V1 shown in Figure 2, the ferroelectric liquid crystal switches from the first stable state to the second stable state; as a result, the amount of light transmis-

sion changes and a white display is produced. This relationship between the amount of light transmission and the applied voltage is shown by a solid line (1) in Figure 5. After that, when a bipolar pulse is applied to the same pixel in the reset period (Rs) shown in Figure 3, the light transmittance drops. Then, the voltage value of the selection pulse applied during the selection period is again increased starting from Vb in Figure 5. The relationship between the amount of light transmission and the applied voltage in this case is as shown by a dashed line (2) in Figure 5. This means that the threshold voltage at which a transition from the first stable state to the second stable state occurs has changed. It is presumed that this change is caused because the state (in this case, the white display state) written to the pixel before the reset period cannot be completely restored to the initial state even after a reset is done in the reset period.

[0029] This slight change in the threshold voltage apparently changes the characteristics of the ferroelectric liquid crystal. That is, the slope of the change of the light transmittance of the ferroelectric liquid crystal, at the time the state changes from the first stable state to the second stable state in response to the applied voltage, was (V2 - V1) in the initial state as shown by the solid line (1) in Figure 5, but because the slope has changed as shown by the dashed line (2), the apparent slope is expressed as shown by a semi-dashed line (3). Accordingly, the apparent slope of the change of the light transmittance of the ferroelectric liquid crystal is now expressed by the value (V2 - Vb). This degradation of the slope increases the absolute value of the signal voltage. As a result, flicker occurs on the display panel during the non-selection period following the selection period, causing problems such as an increased load on the drive circuit.

[0030] The present invention aims at preventing such a threshold voltage change occurring in a short period of time and resolving the problem of the image sticking phenomenon.

[0031] To prevent the threshold voltage change and resolve the problem of the image sticking phenomenon, the inventor investigated voltage waveforms applied during the reset period. As shown in Figure 6, the reset period (Rs) was divided into two periods, a switching period (Sw) and a non-switching period (NSw); in this condition, a bipolar pulse, for example, was applied during the switching period, and a voltage of 0 V, i.e., no voltage, was applied during the non-switching period. Then, the light transmittance was measured while varying the voltage value of the selection pulse applied during the selection period (Se) for various lengths ΔT of NSw. The results showed that the amount of change of the threshold value varied with the length ΔT as shown by the graph of Figure 7.

[0032] The graph of Figure 7 shows V1-Vb as a function of ΔT , that is, the relationship between ΔT and the amount of change of the threshold voltage. As shown in Figure 7, the amount of change of the thresh-

old voltage decreases as ΔT is increased. The longer ΔT , the closer to 0 the amount of change of the threshold voltage becomes.

[0033] The values taken along the ordinate and abscissa of Figure 7 vary depending on the ferroelectric liquid crystal material used.

[0034] The primary intent of the reset period is to prevent the orientation state and electrical properties of the liquid crystal molecules from varying from pixel to pixel before the selection period is begun. It can, however, be seen that if ΔT is not provided or the length is too short, the effect of the reset period is reduced.

[0035] A detailed mechanism that explains why the provision of the non-switching period (NSw) ΔT contributes to reducing the threshold voltage change not clearly known yet. However, an attempt will be made to infer the mechanism by using the models shown in Figures 8 and 9. Figure 8(a) shows the spontaneous polarization of the ferroelectric liquid crystal molecules and the state of ionic impurities in one pixel when the pixel was driven in the white display state before the reset period. On the other hand, Figure 9(a) shows the state when the pixel was driven in the black display state before the reset period. Before the reset pulse is applied, the ferroelectric liquid crystal molecules in the pixel are aligned in one direction according to the display state before the application of the reset pulse, and the ionic impurities are clustered near the alignment films due to the action of the internal electric field (EPs) formed by the spontaneous polarization of the liquid crystal molecules.

[0036] Next, when the reset pulse (external electric field E_{out}) is applied, switching occurs in the ferroelectric liquid crystal molecules, and they switch to the state shown in Figure 8(b) or 9(b). At this time, as shown in Figures 8(b) and 9(b), the direction of the spontaneous polarization of the liquid crystal molecules is the same, but the ionic impurities clustered near the alignment films remain there, being unable to move readily. At this time, the positions of the positive ions and negative ions of the ionic impurities in Figure 8(b) are different from those in Figure 9(b), that is, the direction of the ion electric field (E_{ion}) is different. In this state, when the selection pulse is applied to the pixel in the selection period to put the pixel in the desired display state, if the direction of the ion electric field (E_{ion}) is the same as the direction of the external electric field E_{out} , the effective electric field E_r applied internally to the liquid crystal cell is expressed as $E_r = E_{ion} + E_{out}$. On the other hand, if the direction of the ion electric field (E_{ion}) is opposite to the direction of the external electric field E_{out} , then $E_r = E_{ion} - E_{out}$. In this way, the direction of the ion electric field (E_{ion}) due to the ionic impurities is different between the pixel that was driven in the black display state and the pixel that was driven in the white display state, and therefore, the value of the effective electric field E_r differs even if the same external electric field (E_{out}) is applied; this presumably causes the change in

the threshold voltage.

[0037] In view of this, as shown in Figure 6, after applying a bipolar pulse as the external electric field (E_{out}) during the switching period (S_w) in the reset period (R_s), a voltage of 0 V is applied for the duration of ΔT in the non-switching period (NSw) to the pixels in the states shown in Figures 8(b) and 9(b). In this case, almost no change occurs in the state shown in Figure 9(b). However, in the state shown in Figure 8(b) for the pixel driven in the white display state, the positive ions and negative ions of the ionic impurities move away from the respective alignment films, as shown in the state of Figure 8(c). When ΔT is made longer, the positive ions and negative ions move into the state shown in Figure 8(d) in such a manner as to cancel the direction of the internal electric field (EPs). As a result, the direction of E_{ion} in Figure 8(d) is the same as that shown in the state of Figure 9(b), and the threshold voltage change is thus reduced.

[0038] In the above example, since the ferroelectric liquid crystal is switched to the black display state (the first stable state) in the reset period, the effect of the non-switching period (NSw) is evident in the case of a pixel driven in the white display state before the reset period, such as the one shown in Figure 8. However, in the case where the ferroelectric liquid crystal is switched, for example, to the white display state (the second stable state) in the reset period, it is presumed that the effect of the non-switching period (NSw) will be evident in the case of a pixel, such as the one shown in Figure 9, that was driven in the black display state before the reset period.

[0039] The voltage applied for the time ΔT in the non-switching period has been described as being 0 V, that is, no voltage application. However, this voltage need only be set so that the ion electric field in the ferroelectric liquid crystal will be formed in such a direction that cancels the direction of the electric field formed by the spontaneous polarization of the liquid crystal molecules. For example, a similar effect can be obtained as long as the voltage is set to a value that does not cause the ferroelectric liquid crystal to switch to the other state, i.e., that does not exceed the threshold voltage of the ferroelectric liquid crystal. It will, however, be effective to set this voltage to 0 V, that is, no voltage application. It has also been found that when not using the display (that is, when leaving the display in a non-operating condition), if the pixels of the liquid crystal panel are all held in the same display state, and further, if this display state is made the same as the display state achieved by the application of the switching pulse in the reset period immediately before the selection period, then the threshold voltage change can be reduced when the liquid crystal is driven the next time, even if ΔT is set to a shorter time. More specifically, if the switching pulse as applied during the reset period is applied to all the pixels at the end of the liquid crystal display operation, the threshold voltage change can be suppressed.

[0040] Embodiments of the present invention will be described in detail below with reference to drawings.

[0041] Figure 10 is a diagram showing the structure of a ferroelectric liquid crystal panel used for the construction of a liquid crystal display apparatus according to the present invention.

[0042] The ferroelectric liquid crystal used in the present invention has two stable states as shown in Figure 2, and switches to the first or second stable state depending on the polarity of the applied voltage.

[0043] The liquid crystal panel used in the present invention, shown in Figure 10, comprises a pair of glass substrates 11a and 11b holding therebetween a ferroelectric liquid crystal layer 10 having a thickness of about 1.7 μm . On the opposing surfaces of the glass substrates are formed scanning electrodes 13a and signal electrodes 13b, over which inorganic alignment films 14a and 14b are deposited. Further, a first polarizer 15a is disposed on the outside surface of one glass substrate so that the polarization axis of the polarizer is parallel to the long axis direction of liquid crystal molecules when the molecules are in the first stable state or in the second stable state, while on the outside surface of the other glass substrate, a second polarizer 15b is arranged with its polarization axis oriented at 90° to the polarization axis of the first polarizer 15a.

[Embodiment 1]

[0044] Figure 11 is a diagram showing a combined driving voltage scheme according to a first embodiment of the present invention. This scheme corresponds to the combined voltage scheme shown in Figure 3(c).

[0045] N=1 shows the combined voltage scheme applied to the pixels on the scanning electrode in the first row, N=2 the combined voltage scheme applied to the pixels on the scanning electrode in the second row, and N=240 the combined voltage scheme applied to the pixels on the scanning electrode in the 240th row. In the present invention, all the pixels were simultaneously placed in the black display state in the reset period (Rs) immediately preceding the period (selection period (Se)) during which necessary display data was written, and after that, writing was performed by applying the selection pulse to each scanning electrode in sequence. As shown in Figure 6, the reset period (Rs) was divided into two periods, the switching period (SW) and the non-switching period (NSw); a bipolar pulse of ± 40 V with a pulse duration of 100 μs was applied during the switching period, and a voltage of 0 V was applied during the non-switching period. This period was followed by the selection period (Se) during which a selection pulse of ± 15 V was applied in the case of a black display and ± 25 V in the case of a white display.

[0046] In this case, since the reset pulse is applied to all the pixels simultaneously, as can be seen from Figure 11, the timing for the application of the selection pulse becomes displaced from one scanning electrode

to the next. As a result, the non-switching period (NSw) becomes gradually longer.

[0047] Though not shown here, provisions were made so that when the power switch of the apparatus was turned off, the bipolar pulse was applied to all the scanning electrodes simultaneously to place all the pixels in the same display state (black display state).

[0048] For the liquid crystal display element described above, measurements were made on the threshold voltage change using the same method as used to obtain the graph of Figure 5. The results are shown in Figure 12. First, the selection pulse of the combined voltage was increased in increments of 0.5 V, starting from 0 V. As a result, when the voltage value of V1 shown in Figure 2 was exceeded, the ferroelectric liquid crystal switched from the first stable state to the second stable state, causing the light transmittance to change and thus producing a white display. The relationship between the light transmittance and the applied voltage at this time is shown by a solid line (1) in Figure 12. Next, the reset pulse having the non-switching period according to the present invention was applied to the same pixel, after which the selection pulse was applied; at this time, the voltage value was increased in increments of 0.5 V, starting from a value smaller than V1. The relationship between the light transmittance and the applied voltage in this case is shown by a dashed line (2) in Figure 12. As shown by the dashed line (2) in Figure 12, the result substantially coincided with the solid line (1). Accordingly, even after producing a white display, the slope of the change of the light transmittance of the ferroelectric liquid crystal, such as shown in Figure 5, did not change, and hence, the threshold voltage remained substantially unaffected.

[Embodiment 2]

[0049] Figure 13 is a diagram showing a combined drive voltage scheme according to a second embodiment of the present invention. In this embodiment, the reset period (Rs) was provided each time display data was written to a pixel. For each scanning line, the reset period was set so that the lengths of the switching period (Sw) and non-switching period (NSw) were respectively the same for all the pixels. The reset period (Rs) was set to 8.2 ms in length for each pixel, and the switching period (Sw) and the non-switching period (NSw) about 8 ms in length were provided. The bipolar pulse applied during the switching period (Sw) and the voltage value applied during the selection period (Se) were the same as those used in the first embodiment. As a result, the amount of change of the threshold voltage was made equal for all the scanning lines, achieving a better display quality.

[0050] In the present embodiment, a black display was produced in the reset period, but as a matter of course, the same results were obtained when a white display was produced in the reset period. Furthermore,

brightness adjustment

000050S

0.05 ms

in the present embodiment, the pixels were driven using the scanning electrodes and signal electrodes, but the same results were obtained in the case of an active matrix display where a drive electrode is provided for each pixel.

Claims

1. A ferroelectric liquid crystal display element comprising a ferroelectric liquid crystal sandwiched between a pair of substrates, wherein a driving scheme for said device includes a reset period, before a selection period, having a switching period during which a voltage exceeding a threshold voltage required to switch said ferroelectric liquid crystal is applied irrespective of display data to be written to a pixel, and a non-switching period during which an ion electric field in said ferroelectric liquid crystal is aligned in such a direction that cancels the direction of an electric field produced by spontaneous polarization of liquid crystal molecules.
2. A ferroelectric liquid crystal display element as claimed in claim 1, wherein a voltage applied during said non-switching period does not exceed the threshold voltage of said ferroelectric liquid crystal.
3. A ferroelectric liquid crystal display element as claimed in claim 1, wherein a voltage applied during said non-switching period is 0 V, that is, no voltage is applied.
4. A ferroelectric liquid crystal display element as claimed in any one of claims 1 to 3, wherein a set of bipolar pulses are applied during said switching period.
5. A ferroelectric liquid crystal display element as claimed in any one of claims 1 to 3, wherein said non-switching period has a length ΔT which is so set as to reduce a change in said threshold voltage to nearly zero.
6. A ferroelectric liquid crystal display element as claimed in any one of claims 1 to 3, wherein, said reset period is set at the same timing for all pixels.
7. A ferroelectric liquid crystal display element as claimed in any one of claims 1 to 3, wherein said reset period is set each time display data is written to a pixel.
8. A ferroelectric liquid crystal display element as claimed in claim 7, wherein the length of said non-switching period is the same for all pixels.
9. A ferroelectric liquid crystal display element as claimed in any one of claims 1 to 3, wherein at the

end of a display operation of said ferroelectric liquid crystal display element, a switching pulse as applied during said reset period is applied to all pixels to set said all pixels in the resulting state.

10. A method for driving a ferroelectric liquid crystal display element, comprising a ferroelectric liquid crystal sandwiched between a pair of substrates, wherein a reset period having a switching period and a non-switching period precedes application of a selection pulse and wherein, during said switching period, a voltage exceeding a threshold voltage required to switch said ferroelectric liquid crystal is applied irrespective of display data to be written to a pixel, and during said non-switching period, an ion electric field in said ferroelectric liquid crystal is aligned in such a direction that it cancels the direction of an electric field produced by spontaneous polarization of liquid crystal molecules.
11. A method for driving a ferroelectric liquid crystal display element as claimed in claim 10, wherein a voltage applied during said non-switching period does not exceed the threshold voltage of said ferroelectric liquid crystal.
12. A method for driving a ferroelectric liquid crystal display element as claimed in claim 10, wherein a voltage applied during said non-switching period is 0 V, that is, no voltage is applied.
13. A method for driving a ferroelectric liquid crystal display element as claimed in any one of claims 10 to 12, wherein said voltage applied during said switching period is a set of bipolar pulses.
14. A method for driving a ferroelectric liquid crystal display element as claimed in any one of claims 10 to 12, wherein said non-switching period has a length ΔT which is so set as to reduce a change in said threshold voltage to nearly zero.
15. A method for driving a ferroelectric liquid crystal display element as claimed in any one of claims 10 to 12, wherein said reset period is set with the same timing for all pixels.
16. A method for driving a ferroelectric liquid crystal display element as claimed in any one of claims 10 to 12, wherein said reset period is set each time display data is written to a pixel.
17. A method for driving a ferroelectric liquid crystal display element, as claimed in claim 16, wherein the length of said non-switching period is the same for all pixels.
18. A method for driving a ferroelectric liquid crystal dis-

play element as claimed in any one of claims 10 to 12 wherein, at the end of a display operation of said ferroelectric liquid crystal display element, a switching pulse as applied during said reset period is applied to all pixels.

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Fig.1

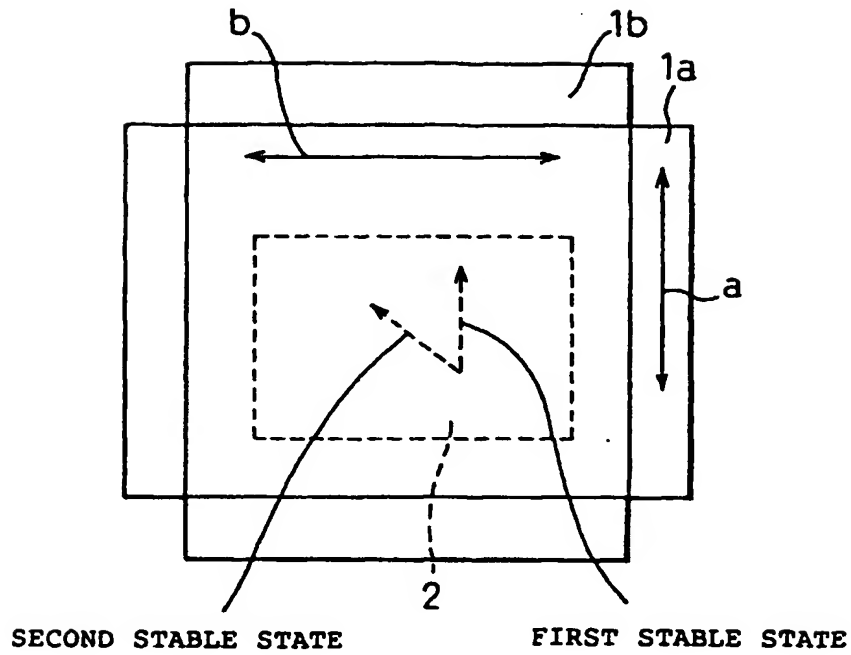


Fig.2

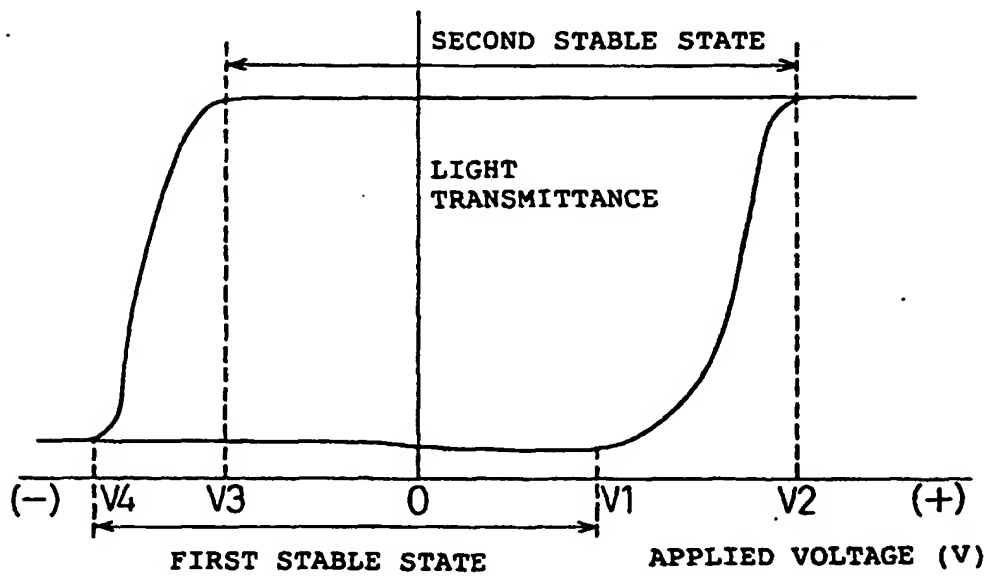


Fig.3

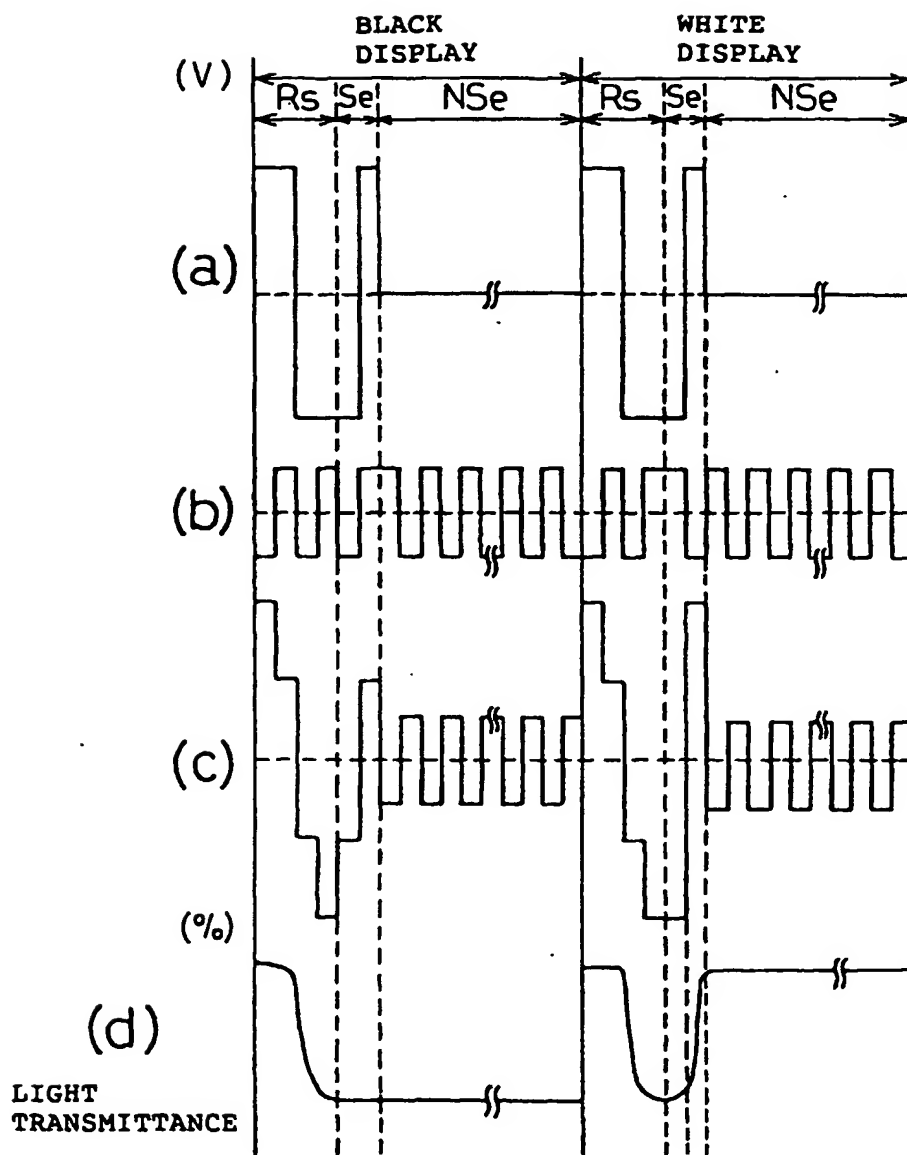


Fig. 4

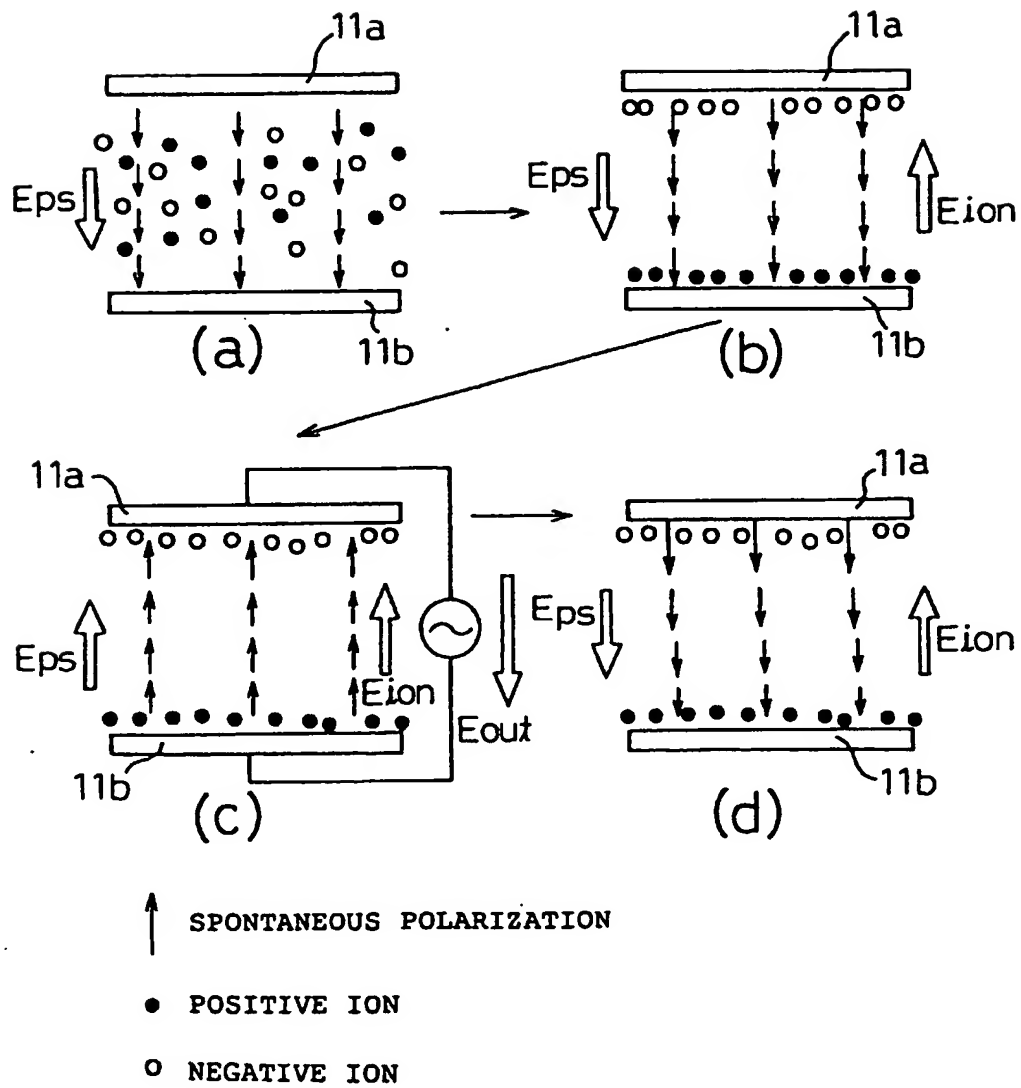


Fig.5

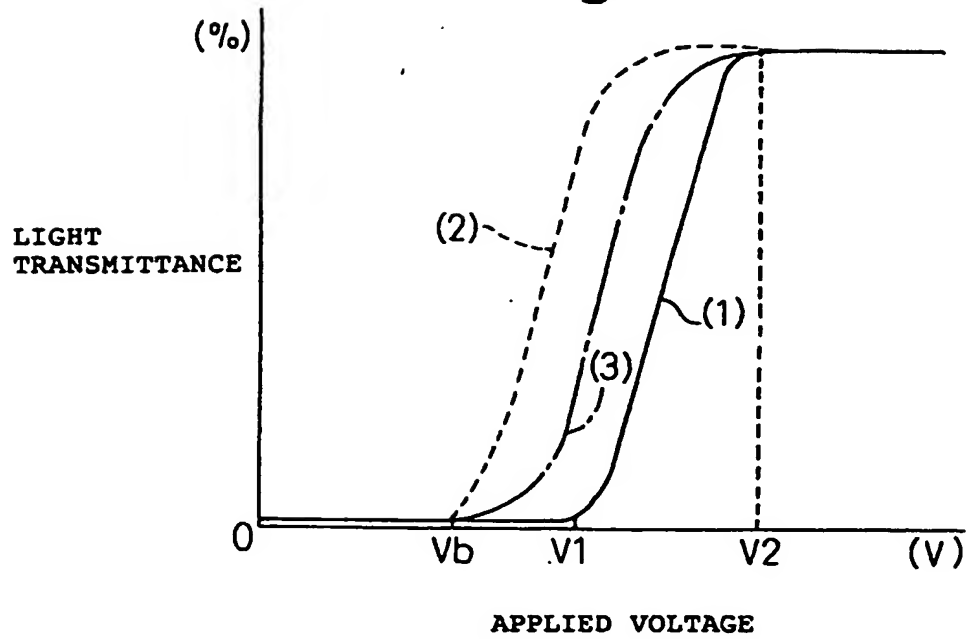


Fig.6

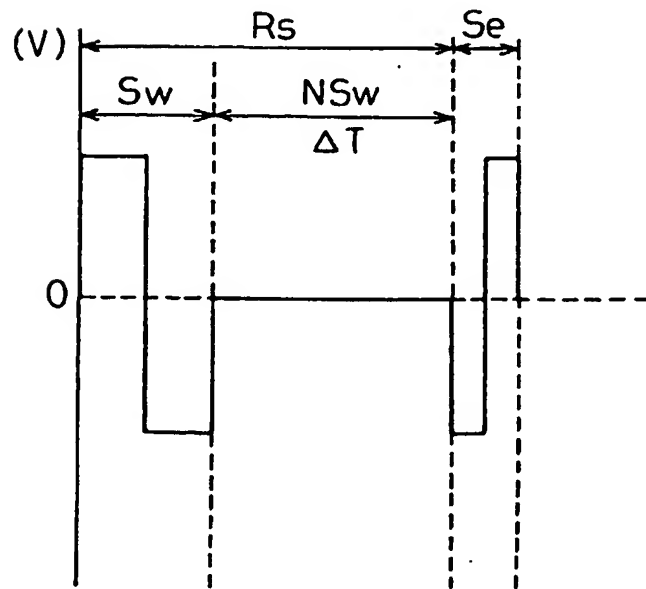


Fig.7

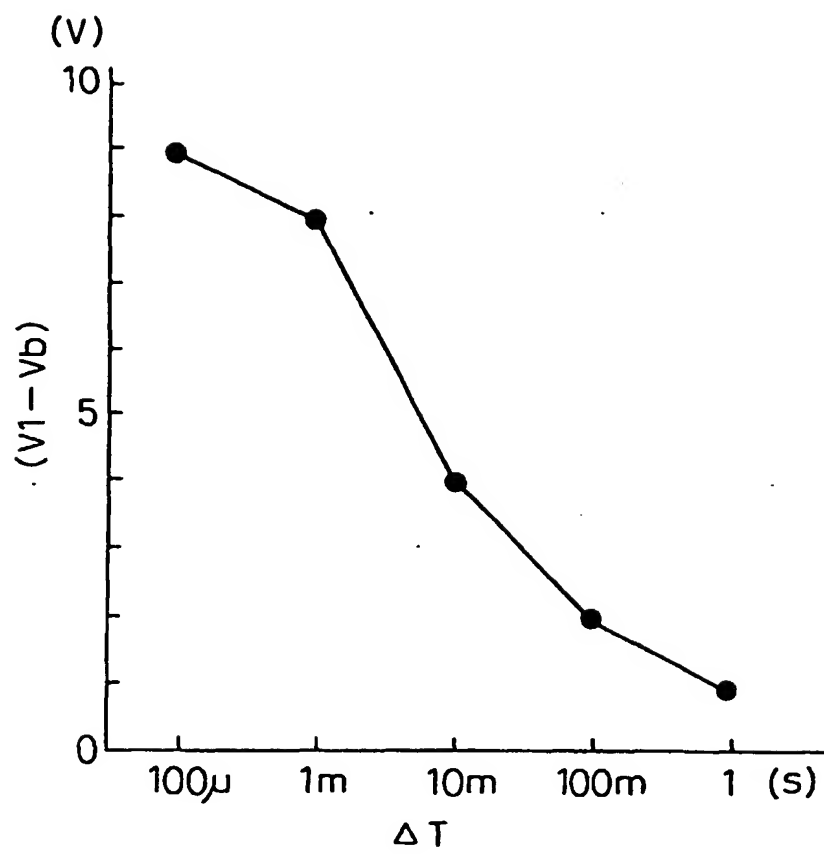


Fig.8

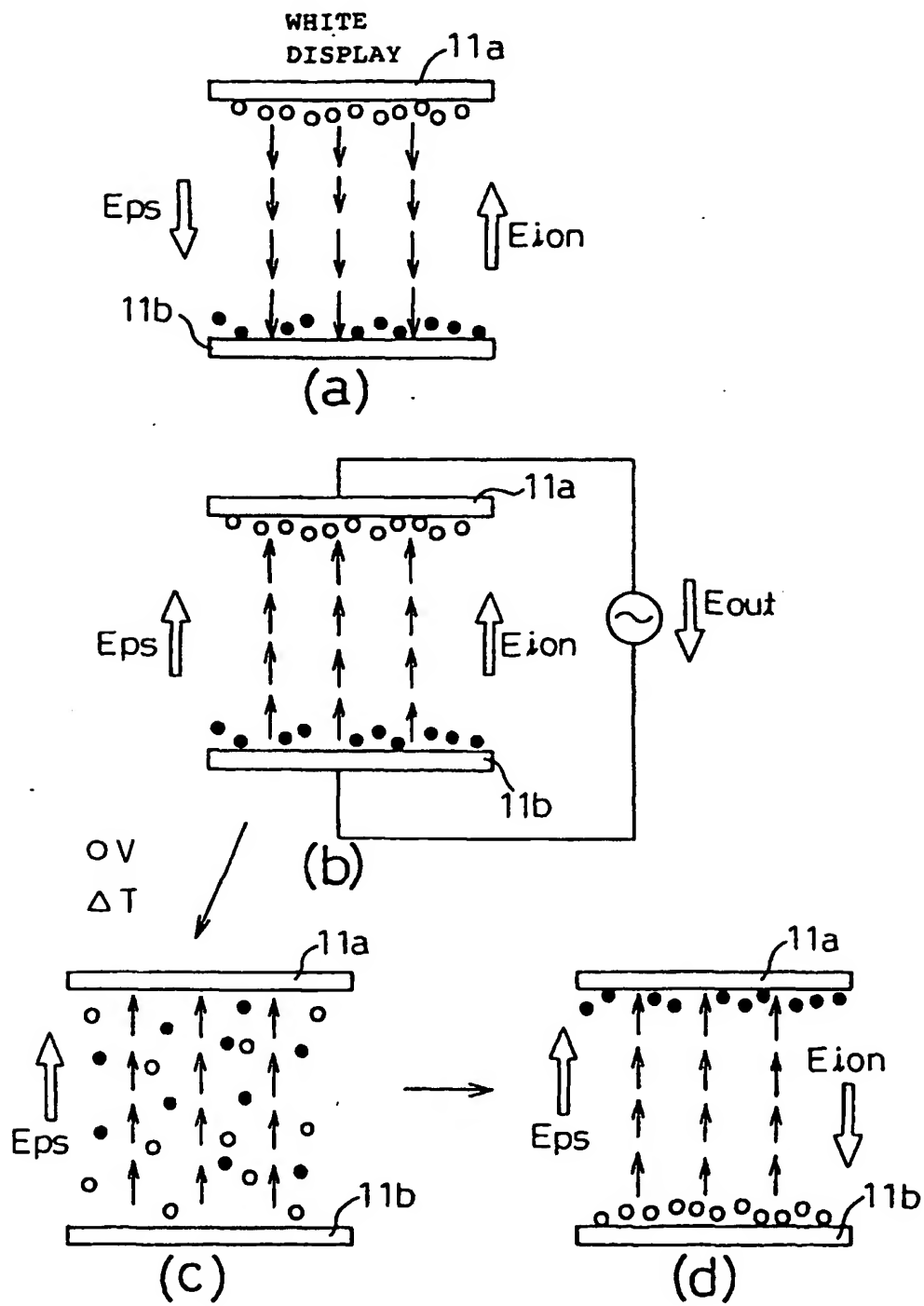


Fig.9

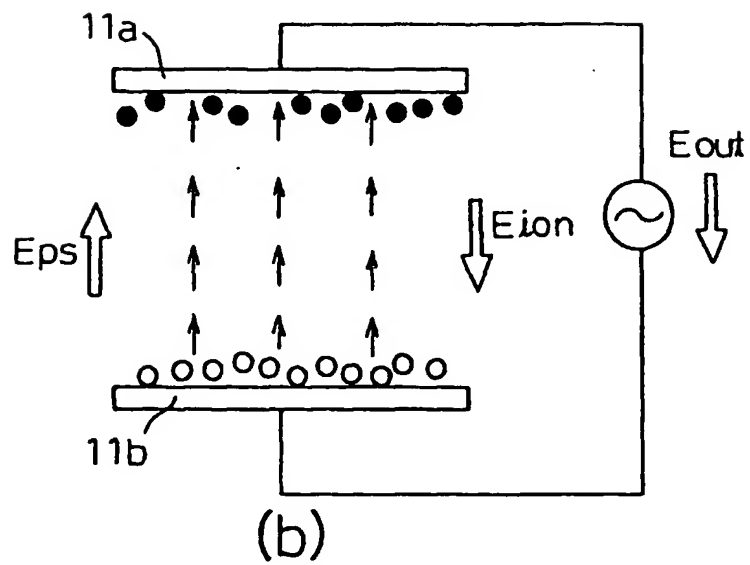
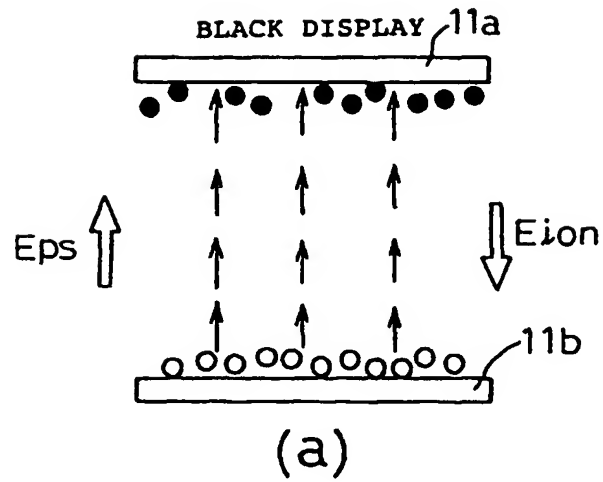


Fig.10

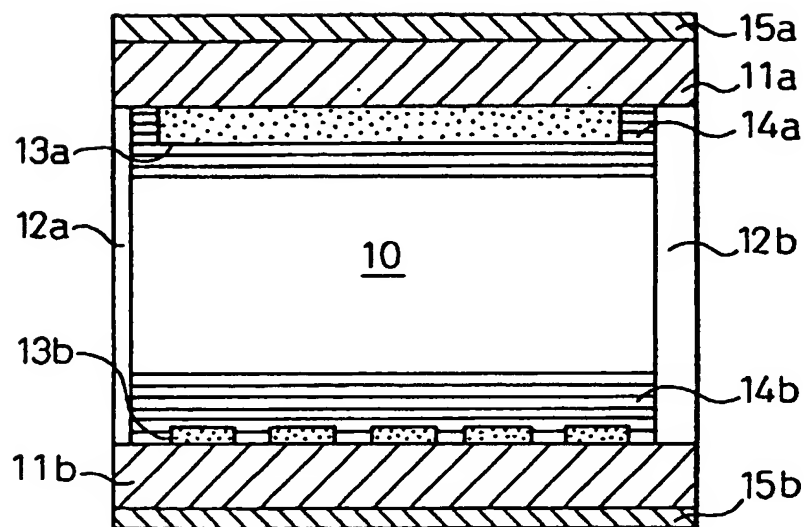


Fig.11

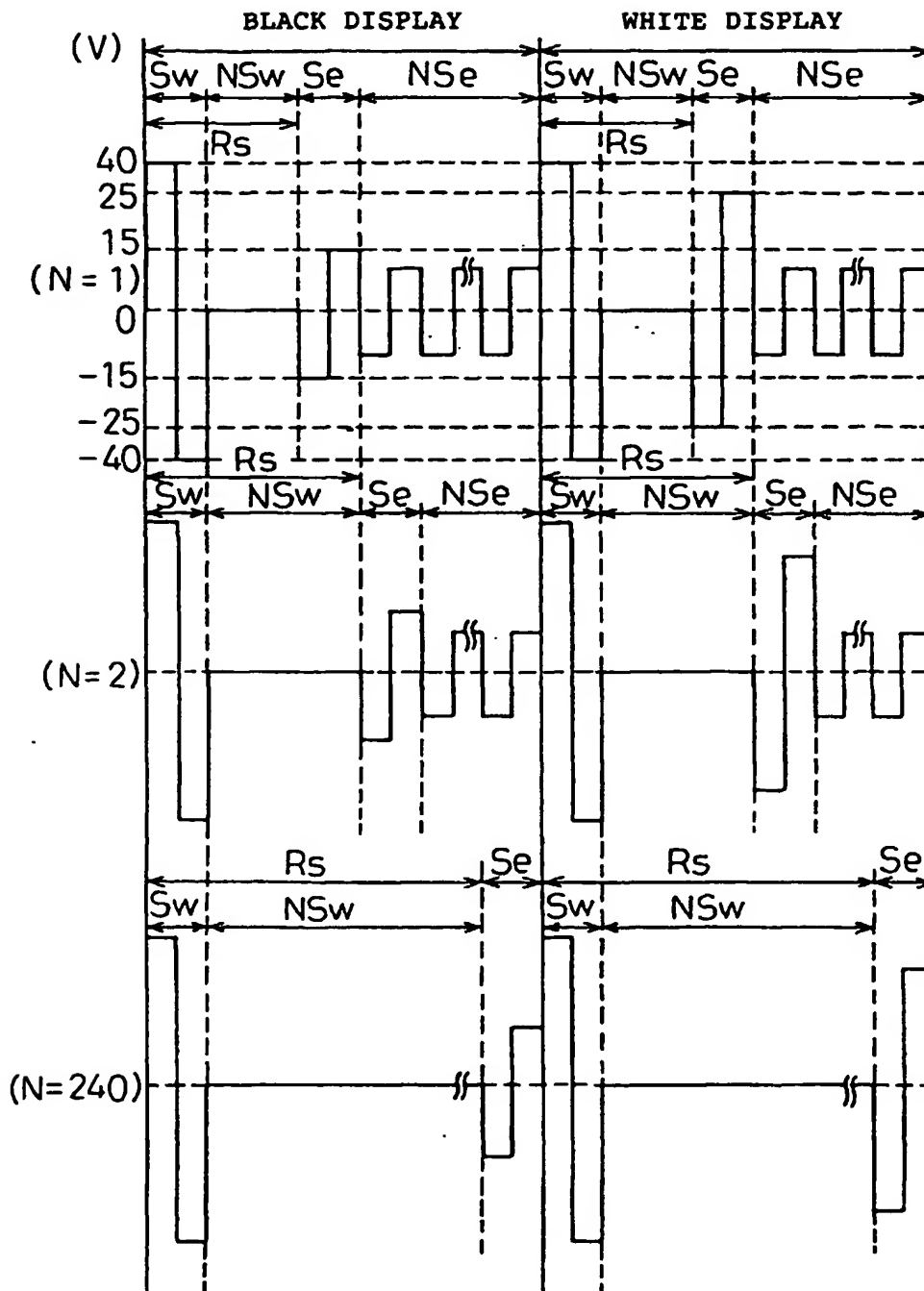


Fig.12

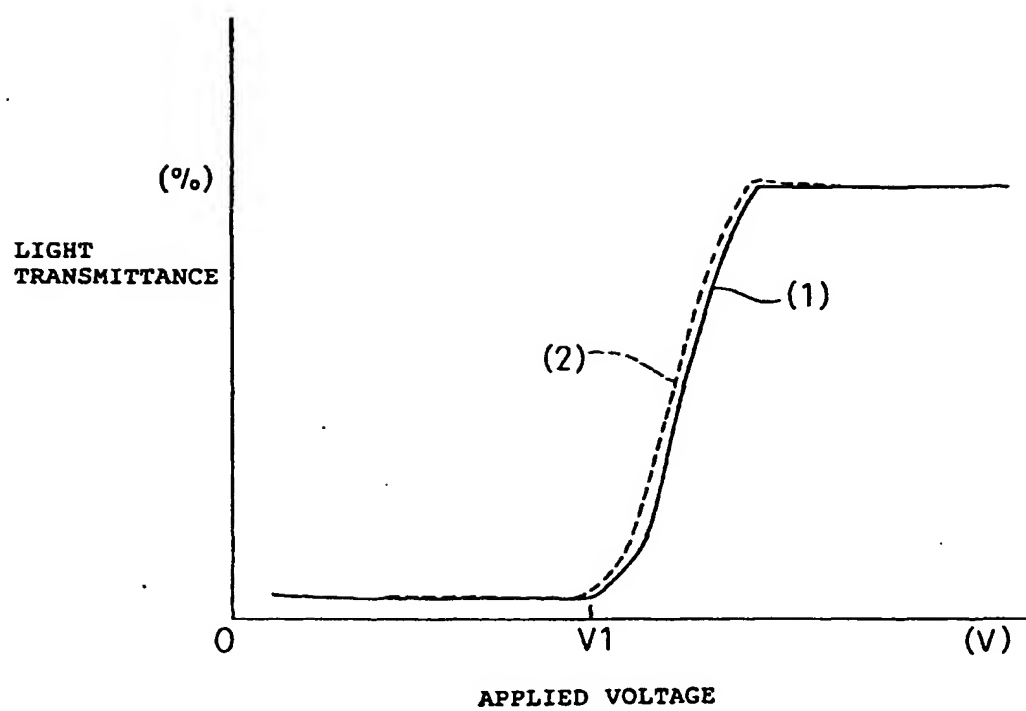
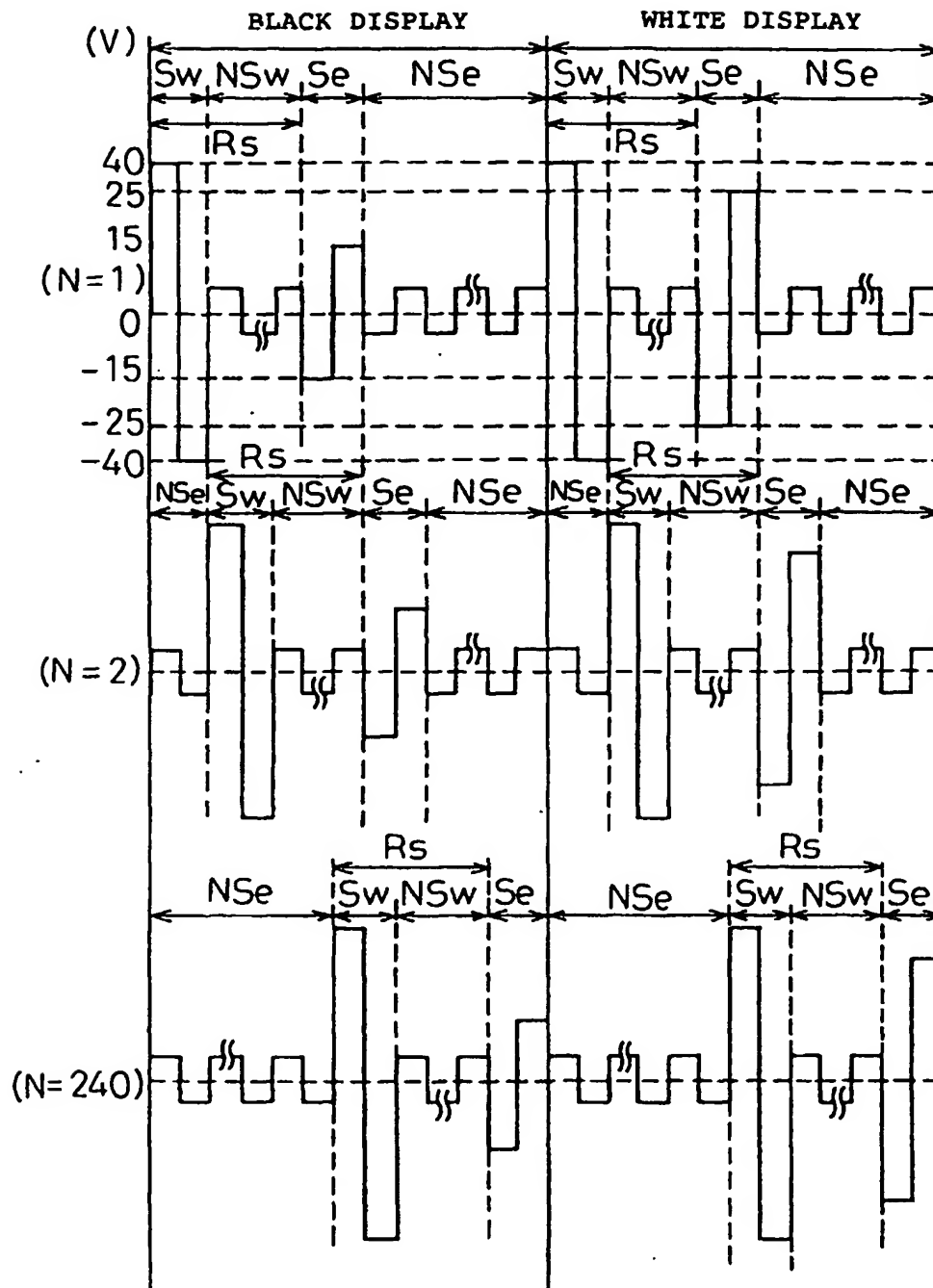


Fig.13



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/05847

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl. ⁷ G02F1/133, G09G3/36		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. ⁷ G02F1/133, G09G3/36		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1922-1996 Toroku Jitsuyo Shinan Koho 1994-2000 Kokai Jitsuyo Shinan Koho 1971-2000 Jitsuyo Shinan Toroku Koho 1996-2000		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP, 469531, A2 (CANON KABUSHIKI KAISHA), 05 February, 1992 (05.02.92), page 5, lines 34-51; Figs. 5, 6	1-2, 4-5, 10-11, 13-14
Y	page 5, lines 34-51; Figs. 5, 6 & JP, 4-85517, A & US, 5379138, A	3, 6-9, 12, 15-18
X	JP, 4-175724, A (Canon Inc.), 23 June, 1992 (23.06.92), Full text; Figs. 1-7	1-3, 5, 7-8, 10-12, 14, 16-17
Y	Full text; Figs. 1-7 (Family: none)	6, 9, 15, 18
A	JP, 5-2376, A (Canon Inc.), 08 January, 1993 (08.01.93), Par. Nos. [0015]-[0021]; Figs. 4, 5 (Family: none)	1-18
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search 13 January, 2000 (13.01.00)		Date of mailing of the international search report 25 January, 2000 (25.01.00)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
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INTERNATIONAL SEARCH REPORT

International application No.

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C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP, 256548, A1 (CANON KABUSHIKI KAISHA), 24 February, 1988 (24.02.88), Full text; Figs. 1-9 & JP, 63-60428, A & US, 4938574, A	3, 6-8, 12, 15-17
Y	EP, 424958, A2 (CANON KABUSHIKI KAISHA), 02 May, 1991 (02.05.91), Full text; Figs. 1-7 & JP, 3-296713, A & US, 5592191, A	9, 18

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